

RD74VT1G125

Bus Buffer Gate with 3-state output / Dual Supply Voltage Translator

REJ03D0496-0100 Rev.1.00 Feb. 01, 2005

Description

The RD74VT1G125 has a bus buffer gate with 3–state output in a 6 pin package. Output is disabled when the associated output enable (\overline{OE}) input is high. To ensure the high impedance state during power up or power down, \overline{OE} should be connected to $V_{CC}IN$ through a pull-up resistor, the minimum value of the resistor is determined by the current sinking capability of the driver. The input is designed to track $V_{CC}IN$, which accepts voltages from 1.2 V to 3.6 V, and the output is designed to track $V_{CC}OUT$, which operates at 1.2 V to 3.6 V. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- This product function as level shift that change $V_{CC}IN$ input level to $V_{CC}OUT$ output level by providing different supply voltage to $V_{CC}IN$ and $V_{CC}OUT$.
- Supplied on emboss taping for high-speed automatic mounting.
- Supply voltage range: $V_{CC}IN = 1.2 \text{ V to } 3.6 \text{ V}$

 $V_{CC}OUT = 1.2 \text{ V to } 3.6 \text{ V}$

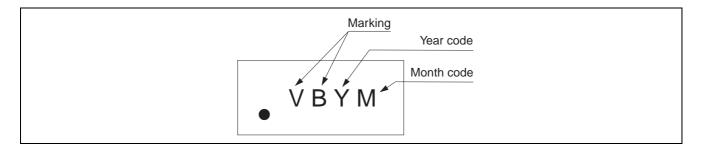
- Operating temperature range: -40° C to $+85^{\circ}$ C
- All inputs $V_{IH}(Max.) = 3.6 \text{ V } (@V_{CC}IN = 0 \text{ V to } 3.6 \text{ V})$ All outputs $V_O(Max.) = 3.6 \text{ V } (@V_{CC}OUT = 0 \text{ V})$
- Output current

 $\begin{array}{l} \pm 2 \text{ mA } (@V_{CC}OUT = 1.2 \text{ V}) \\ \pm 4 \text{ mA } (@V_{CC}OUT = 1.4 \text{ V to } 1.6 \text{ V}) \\ \pm 6 \text{ mA } (@V_{CC}OUT = 1.65 \text{ V to } 1.95 \text{ V}) \\ \pm 18 \text{ mA } (@V_{CC}OUT = 2.3 \text{ V to } 2.7 \text{ V}) \\ \pm 24 \text{ mA } (@V_{CC}OUT = 3.0 \text{ V to } 3.6 \text{ V}) \end{array}$

Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
RD74VT1G125CLE	WCSP-6 pin	SXBG0006KB-A (TBS-6AV)	CL	E (3,000 pcs / reel)

Article Indication



Function Table

Inp	Inputs					
ŌĒ	Α	OUTPUT Y				
L	Н	Н				
L	L	L				
Н	X	Z				

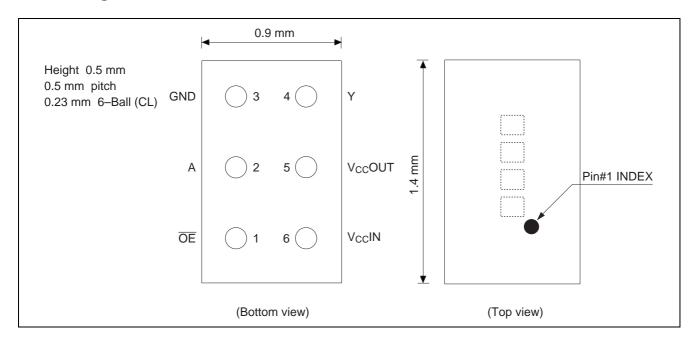
H: High level

L: Low level

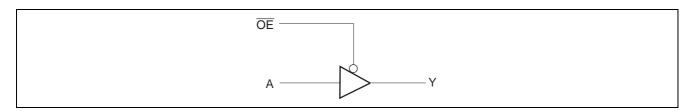
X: Immaterial

Z: High impedance

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V _{CC} IN, V _{CC} OUT	-0.5 to 4.6	V	
Input voltage range *1	Vı	-0.5 to 4.6	V	A port or OE
Output voltage range *1, 2	Vo	-0.5 to V _{CC} OUT+0.5	V	Output: "H" or "L"
		-0.5 to 4.6		V _{CC} OUT: OFF
Input clamp current	I _{IK}	– 50	mA	V ₁ < 0
Output clamp current	I _{OK}	– 50	mA	V _O < 0
		50		$V_{\rm O} > V_{\rm CC} + 0.5$
Continuous output current	I ₀	±50	mA	
Continuous output current V _{CC} or GND	I _{CC} IN, I _{CC} OUT, I _{GND}	±100	mA	
Package Thermal impedance	θ_{ja}	123	°C/W	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 4.6 V maximum.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V _{CC} IN	1.2 to 3.6	V	
	VccOUT	1.2 to 3.6		
Input/Output voltage	VI	0 to 3.6	V	A port or OE
	Vo	0 to 3.6	V	Output: "H" or "L" or "Z"
		0 to V _{CC} OUT		V _{CC} OUT: OFF
Output current	I _{OH}	-2	mA	V _{CC} OUT = 1.2 V
		-4		V _{CC} OUT = 1.5±0.1 V
		-6		V _{CC} OUT = 1.8±0.15 V
		-18		V _{CC} OUT = 2.5±0.2 V
		-24		$V_{CC}OUT = 3.3\pm0.3 \text{ V}$
	I _{OL}	2	mA	V _{CC} OUT = 1.2 V
		4		V _{CC} OUT = 1.5±0.1 V
		6		V _{CC} OUT = 1.8±0.15 V
		18		V _{CC} OUT = 2.5±0.2 V
		24		V _{CC} OUT = 3.3±0.3 V
Input transition rise or fall time	Δt / Δν	10	ns / V	
Operation free-air temperature	Та	-40 to 85	°C	

Electrical Characteristics

 $(Ta = -40 \text{ to } 85^{\circ}C)$

Item	Symbol	V _{CC} IN (V) *	V _{CC} OUT (V) *	Min	Тур	Max	Unit	Test conditions
Input voltage	V_{IH}	1.2	1.2 to 3.6	V _{CC} IN×0.75	_		V	A port
		1.5±0.1		V _{CC} IN×0.70	_	_		Control input
		1.8±0.15]	V _{CC} IN×0.65	_	_		
		2.5±0.2]	1.6	_	_		
		3.3±0.3]	2.0		_		
	V_{IL}	1.2	1.2 to 3.6	_	_	V _{CC} IN×0.25	V	A port
		1.5±0.1		_	_	V _{CC} IN×0.30		Control input
		1.8±0.15		_	_	V _{CC} IN×0.35		
		2.5±0.2			_	0.7		
		3.3±0.3			_	8.0		
Output voltage	V _{OH}	1.2 to 3.6	1.2 to 3.6	V _{CC} OUT-0.2	_		V	$I_{OH} = -100 \mu A$
			1.2	0.9	_			$I_{OH} = -2 \text{ mA}$
			1.5±0.1	1.1	_			$I_{OH} = -4 \text{ mA}$
			1.8±0.15	1.25	_			$I_{OH} = -6 \text{ mA}$
			2.5±0.2	1.7	_			$I_{OH} = -18 \text{ mA}$
			3.3±0.3	2.2	_			$I_{OH} = -24 \text{ mA}$
	V_{OL}	1.2 to 3.6	1.2 to 3.6		_	0.2	V	$I_{OL} = 100 \mu A$
			1.2	_	_	0.3		$I_{OL} = 2 \text{ mA}$
			1.5±0.1	_	_	0.3		$I_{OL} = 4 \text{ mA}$
			1.8±0.15	_	_	0.3		$I_{OL} = 6 \text{ mA}$
			2.5±0.2	_	_	0.6		$I_{OL} = 18 \text{ mA}$
			3.3±0.3	_	_	0.55		$I_{OL} = 24 \text{ mA}$
Input current	I _{IN}	3.6	3.6	-1.0	_	1.0	μΑ	$V_{IN} = GND \text{ or } V_{CC}IN$
								control input
Off state output current	l _{OZ}	3.6	3.6	-1.5	_	1.5	μΑ	$V_{IN} = V_{IH} \text{ or } V_{IL}$
Output leakage current	I _{OFF}	0	0	_	_	1.5	μΑ	V _{IN} , V _{OUT} = 0 to 3.6 V
Quiescent supply current	I _{CC} IN	1.2 to 3.6	1.2 to 3.6	-3.0	_	3.0	μΑ	$I_{O(Y \text{ port})} = 0$ $V_{IN} = V_{CC}IN \text{ or GND}$
	I _{CC} OUT	1.2 to 3.6	1.2 to 3.6	-3.0	_	3.0		$I_{O(Y \text{ port})} = 0$ $V_{IN} = V_{CC}IN \text{ or GND}$
Increase in I _{CC} per input	ΔI_{CC}	3.6	3.6	_	_	250	μΑ	A port or control V _{CC} IN–0.6 (1 input)
Input capacitance	C _{IN}	3.3	3.3	_	3.5	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

 $V_{CC}IN = 3.3\pm0.3 \text{ V}$

				V _{cc} OUT=	VccC	UT=	V _{cc} C	UT=	V _{cc} C	UT=	VccC	DUT=		
		From	То	1.2 V	1.5±	0.1 V	1.8±0	.15 V	2.5±	0.2 V	3.3±	0.3 V		Test
Item	Symbol	(input)	(output)	Тур	Min	Max	Min	Max	Min	Max	Min	Max	Unit	conditions
Propagation	t _{PLH}	Α	Υ	9.2	2.0	8.8	1.5	5.8	1.0	4.2	1.0	3.5	ns	C _L = 15pF
delay time	t _{PHL}			9.2	2.0	8.8	1.5	5.8	1.0	4.2	1.0	3.5		$R_L = 2.0k\Omega$
Output	t _{zH}	ŌĒ	Υ	10.2	2.0	9.6	1.5	6.4	1.0	4.2	1.0	3.7	ns	C _L = 15pF
enable time	t _{ZL}			10.2	2.0	9.6	1.5	6.4	1.0	4.2	1.0	3.7		$R_L = 2.0k\Omega$
Output	t _{HZ}	ŌĒ	Υ	5.2	2.0	5.6	1.5	5.2	1.0	4.6	1.0	4.5	ns	C _L = 15pF
disable time	t_{LZ}			5.2	2.0	5.6	1.5	5.2	1.0	4.6	1.0	4.5		$R_L = 2.0k\Omega$

 $V_{CC}IN = 2.5 \pm 0.2 \text{ V}$

					Ta = −40 to 85°C									
				V _{cc} OUT=	VccC	UT=	VccC	UT=	V _{cc} C	=TU	VccC	UT=		
		From	То	1.2 V	1.5±0	0.1 V	1.8±0	.15 V	2.5±	0.2 V	3.3±	0.3 V		Test
Item	Symbol	(input)	(output)	Тур	Min	Max	Min	Max	Min	Max	Min	Max	Unit	conditions
Propagation	t _{PLH}	Α	Υ	9.5	2.0	9.0	1.5	6.0	1.0	4.4	1.0	3.7	ns	C _L = 15pF
delay time	t _{PHL}			9.5	2.0	9.0	1.5	6.0	1.0	4.4	1.0	3.7		$R_L = 2.0k\Omega$
Output	t _{ZH}	ŌĒ	Υ	10.6	2.0	10.2	1.5	6.6	1.0	4.5	1.0	3.8	ns	C _L = 15pF
enable time	t _{ZL}			10.6	2.0	10.2	1.5	6.6	1.0	4.5	1.0	3.8		$R_L = 2.0k\Omega$
Output	t _{HZ}	ŌĒ	Υ	5.4	2.0	5.7	1.5	5.3	1.0	4.5	1.0	4.4	ns	C _L = 15pF
disable time	t _{LZ}			5.4	2.0	5.7	1.5	5.3	1.0	4.5	1.0	4.4		$R_L = 2.0k\Omega$

 $V_{CC}IN = 1.8\pm0.15 \text{ V}$

					Ta = −40 to 85°C									
				V _{cc} OUT=	V _{cc} C	UT=	VccC	UT=	V _{cc} C	UT=	V _{cc} C	UT=		
		From	То	1.2 V	1.5±	0.1 V	1.8±0	.15 V	2.5±	0.2 V	3.3±	0.3 V		Test
Item	Symbol	(input)	(output)	Тур	Min	Max	Min	Max	Min	Max	Min	Max	Unit	conditions
Propagation	t _{PLH}	Α	Y	9.6	2.0	9.2	1.5	6.5	1.0	4.7	1.0	4.0	ns	C _L = 15pF
delay time	t _{PHL}			9.6	2.0	9.2	1.5	6.5	1.0	4.7	1.0	4.0		$R_L = 2.0k\Omega$
Output	t _{zH}	ŌĒ	Y	10.8	2.0	10.8	1.5	7.0	1.0	5.2	1.0	4.5	ns	C _L = 15pF
enable time	t _{ZL}			10.8	2.0	10.8	1.5	7.0	1.0	5.2	1.0	4.5		$R_L = 2.0k\Omega$
Output	t _{HZ}	ŌĒ	Y	5.8	2.0	6.0	1.5	5.8	1.0	5.4	1.0	5.2	ns	C _L = 15pF
disable time	t _{LZ}			5.8	2.0	6.0	1.5	5.8	1.0	5.4	1.0	5.2		$R_L = 2.0k\Omega$

 $V_{CC}IN=1.5{\pm}0.1~V$

				V _{CC} OUT=	VccC	UT=	VccC	UT=	VccC	UT=	VccC	DUT=		
		From	То	1.2 V	1.5±	0.1 V	1.8±0).15 V	2.5±	0.2 V	3.3±	0.3 V		Test
Item	Symbol	(input)	(output)	Тур	Min	Max	Min	Max	Min	Max	Min	Max	Unit	conditions
Propagation	t _{PLH}	Α	Υ	9.8	2.0	10.0	1.5	6.9	1.0	5.1	1.0	4.5	ns	C _L = 15pF
delay time	t _{PHL}			9.8	2.0	10.0	1.5	6.9	1.0	5.1	1.0	4.5		$R_L = 2.0k\Omega$
Output	t _{zH}	ŌĒ	Υ	11.2	2.0	11.2	1.5	7.8	1.0	5.4	1.0	4.8	ns	C _L = 15pF
enable time	t _{ZL}			11.2	2.0	11.2	1.5	7.8	1.0	5.4	1.0	4.8		$R_L = 2.0k\Omega$
Output	t _{HZ}	ŌĒ	Υ	6.4	2.0	7.2	1.5	6.4	1.0	5.8	1.0	5.6	ns	C _L = 15pF
disable time	t _{LZ}			6.4	2.0	7.2	1.5	6.4	1.0	5.8	1.0	5.6		$R_L = 2.0k\Omega$

Switching Characteristics (Cont.)

 $V_{CC}IN = 1.2 V$

					Ta = -40 to 85°C								
				V _{cc} OUT=									
		From	То	1.2 V	1.5±0.1 V	1.8±0.15 V	2.5±0.2 V	3.3±0.3 V		Test			
Item	Symbol	(input)	(output)	Тур	Тур	Тур	Тур	Тур	Unit	conditions			
Propagation	t _{PLH}	Α	Y	10.5	7.5	6.0	4.5	4.0	ns	$C_L = 15pF$			
delay time	t _{PHL}			10.5	7.5	6.0	4.5	4.0		$R_L = 2.0k\Omega$			
Output	t _{ZH}	ŌĒ	Υ	11.6	8.5	6.5	5.0	4.2	ns	C _L = 15pF			
enable time	t _{ZL}			11.6	8.5	6.5	5.0	4.2		$R_L = 2.0k\Omega$			
Output	t _{HZ}	ŌĒ	Υ	7.0	6.2	6.0	5.7	5.5	ns	C _L = 15pF			
disable time	t _{LZ}			7.0	6.2	6.0	5.7	5.5		$R_L = 2.0k\Omega$			

Operating Characteristics

 $Ta = 25^{\circ}C$

Item	Symbol	V _{cc} IN (V)	V _{CC} OUT (V)	Min	Тур	Max	Unit	Test conditions
Power dissipation	C_{PD}	3.3	3.3	_	12	_	pF	f = 10 MHz
capacitance								$C_L = 0$

Power-up considerations

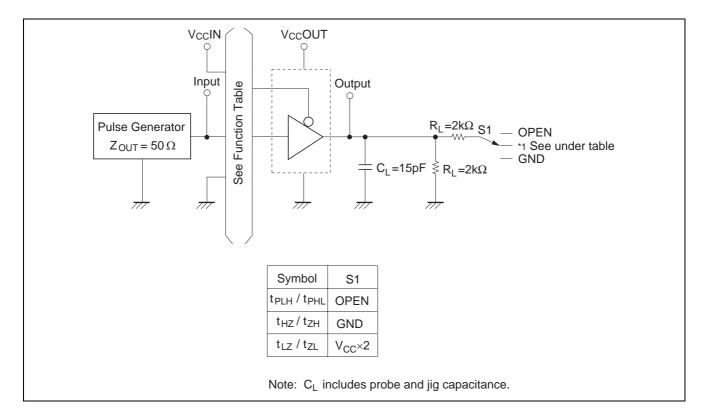
Level-translation devices offer an opportunity for successful mixed-voltage signal design.

A proper power-up sequence always should be followed to avoid excessive supply current, bus contention, oscillations, or other anomalies caused by improperly biased device pins.

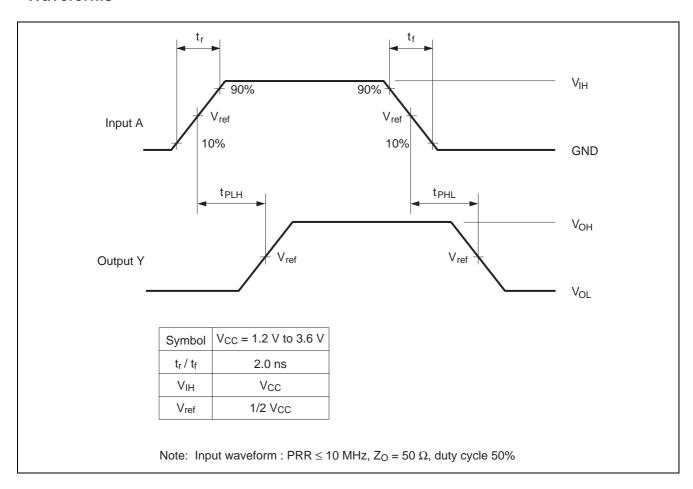
Take these precautions to guard against such power-up problems.

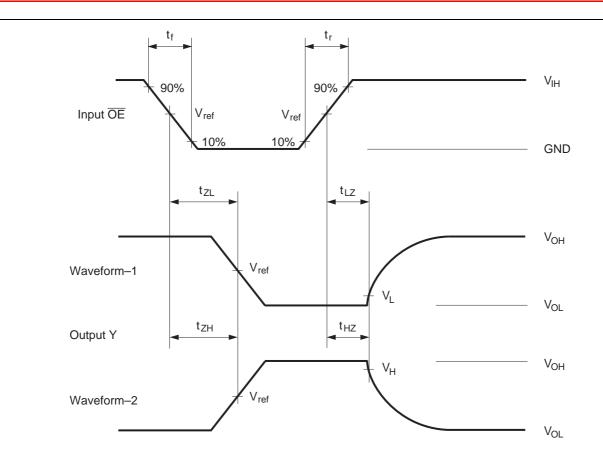
- 1. Connect ground before any supply voltage is applied.
- 2. Next, power up the control side of the device. (Power up of $V_{\rm CC}IN$ is first. Next power up is $V_{\rm CC}OUT$)
- 3. Tie \overline{OE} to $V_{CC}IN$ with a pull-up resistor so that it ramps with $V_{CC}IN$.

Test Circuit



Waveforms



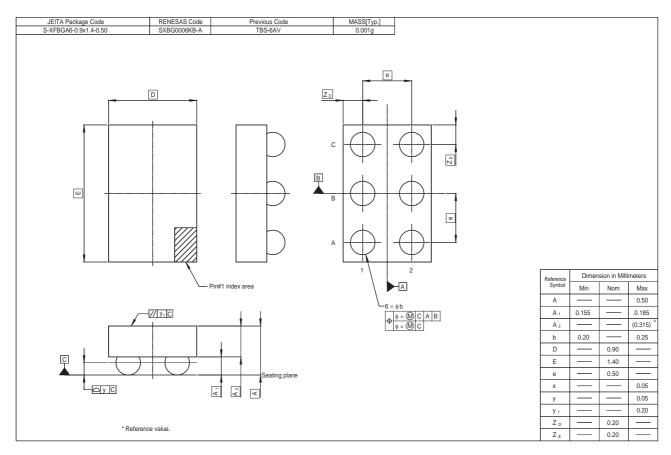


Symbol	V _{CC} = 1.2 V, 1.5±0.1 V	V _{CC} = 1.8±0.15 V	V _{CC} = 2.5±0.2 V	V _{CC} = 3.3±0.3 V
t _r / t _f	2.0 ns	2.0 ns	2.0 ns	2.0 ns
V _{IH}	Vcc	Vcc	Vcc	Vcc
V _{ref}	1/2 V _{CC}	1/2 V _{CC}	1/2 V _{CC}	1/2 V _{CC}
V _H / V _L	$V_H = V_{OH}-0.1 V$ $V_L = V_{OL}+0.1 V$	$V_H = V_{OH}$ -0.15 V $V_L = V_{OL}$ +0.15 V	$V_H = V_{OH} - 0.15 V$ $V_L = V_{OL} + 0.15 V$	$V_{H} = V_{OH} - 0.3 V$ $V_{L} = V_{OL} + 0.3 V$

Notes: 1. Input waveform : PRR \leq 10 MHz, Zo = 50 $\Omega,$ duty cycle 50%.

- 2. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
- 3. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. The output are measured one at a time with one transition per measurement.

Package Dimensions



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